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Informal Patent Application Summary (PTO-413), Io./Mail Date r's Amendment/Comment r's Statement of Reasons for Allowance

Reasons for Allowance

- 1. Claims 3-9 and 13-15 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose all the limitations recited in the base claim 13. Specifically, the combination of an integrated-circuit device comprising: a vertical JFET, including: a first and second gate regions in a first region near the top surface of the substrate; the side surfaces of the first and second gate regions being substantially parallel to each other and substantially perpendicular to the top surface of the substrate; a channel region in the first region, between the first and the second gate regions having side surfaces adjacent to the side surfaces of the gate regions, a top surface, and a bottom surface; the top surface of the channel region electrically communicable to a source terminal, the bottom surface of the channel region electrically communicable to a drain terminal; the gate terminal, the source terminal, and the drain terminal being near the top surface of the substrate; the first gate region having a pill-box shape with a substantially flat top surface and bottom surface, and a side surface substantially perpendicular to the top, and bottom surfaces; the channel region having a ring shape enclosing the first gate region, a bottom surface substantially coplanar to the bottom surface of the first gate region and a top surface substantially coplanar to the top surface of the first gate region; the second gate region having a ring shape enclosing the channel region, a bottom surface substantially coplanar to the bottom surface of the first Art Unit: 2814

gate region and a top surface substantially coplanar to the top surface of the first gate region; and a drain plug region having a ring shape enclosing the second gate region, a bottom surface substantially coplanar to the bottom surface of the first gate region and a top surface substantially coplanar to the top surface of the first gate region.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Theresa T. Doan whose telephone number is (571) 272-1704. The examiner can normally be reached on Monday to Friday from 7:00AM - 4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, WAEL FAHMY can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For

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Center (EBC) at 866-217-9197 (toll-free).

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Theresa Doan August 30, 2006.